

#24D/10-7-03  
V. Jones

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
Bulent DERVISOGLU et al.

Appl. No. 09/275,726  
Confirmation No. 1134

Filed: March 24, 1999

For: ON-CHIP SERVICE PROCESSOR  
FOR TEST AND DEBUG OF  
INTEGRATED CIRCUITS

Art Unit: 2133

Examiner: David Ton

Atty. Docket No. 41567-192232

Customer No.

26694  
PATENT TRADEMARK OFFICE

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Technology Center 2100

**Amendment and Reply Under 37 C.F.R. §§ 1.111 and 1.121**

Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the Non-final Office Action (Office Action) dated **July 9, 2003**, (PTO Prosecution File Wrapper Paper No. 22), Applicants submit the following Amendment and Reply.

It is not believed that extensions of time or fees for net addition of claims are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are needed to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a). Any fees required therefor (including fees for net addition of claims), and any other fee deficiency, are hereby authorized to be charged, or any overpayments credited, to our Deposit Account No. 22-0261.

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Applicant(s): DERVISOGLU et al.  
Appl. No. 09/275,726

***Amendments***

Please enter the amendments to the application as follows:

**Amendments to the Claims** begin on Page 3 of this paper.

**Remarks** begin on Page 10 of this paper.

